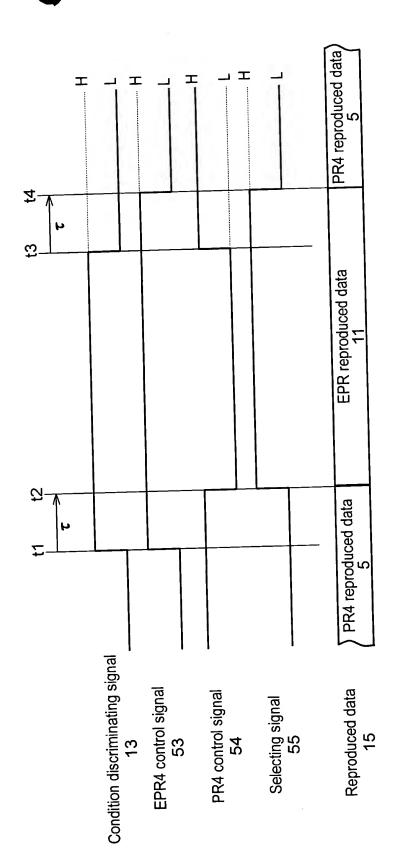


Selector 55 S) 20 PR4 Viterbi decoder ,54 Timing control signal generator -52 **EPR4** Viterbi decoder 53/ 5 5 Signal condition discriminating circuit တ 71 EPR4 converting filter 'ဖ . ന PR4 equalizer

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FIG. 3

FIG. 4



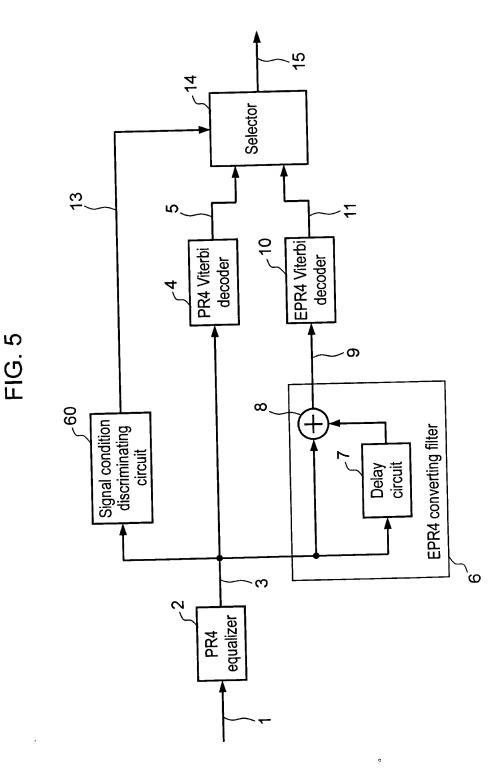
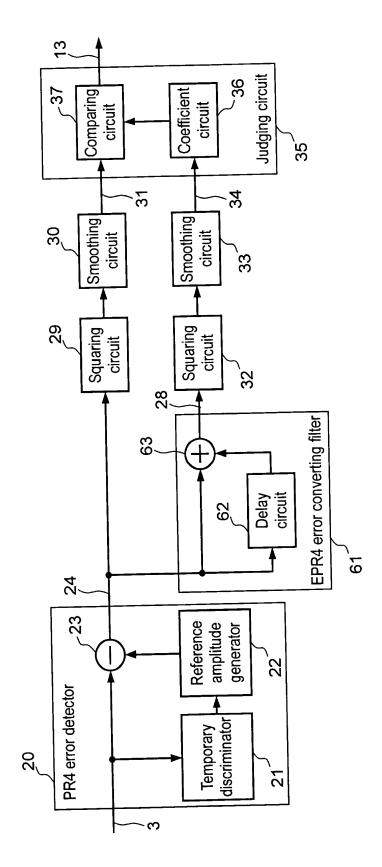


FIG. 6



Selector 55 S. -50 PR4 Viterbi decoder 54 Timing control signal generator 25 EPR4 Viterbi decoder FIG. 7 53 51 13 တ Signal condition discriminating circuit converting filter EPR4 09 64 . ന equalizer PR4

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